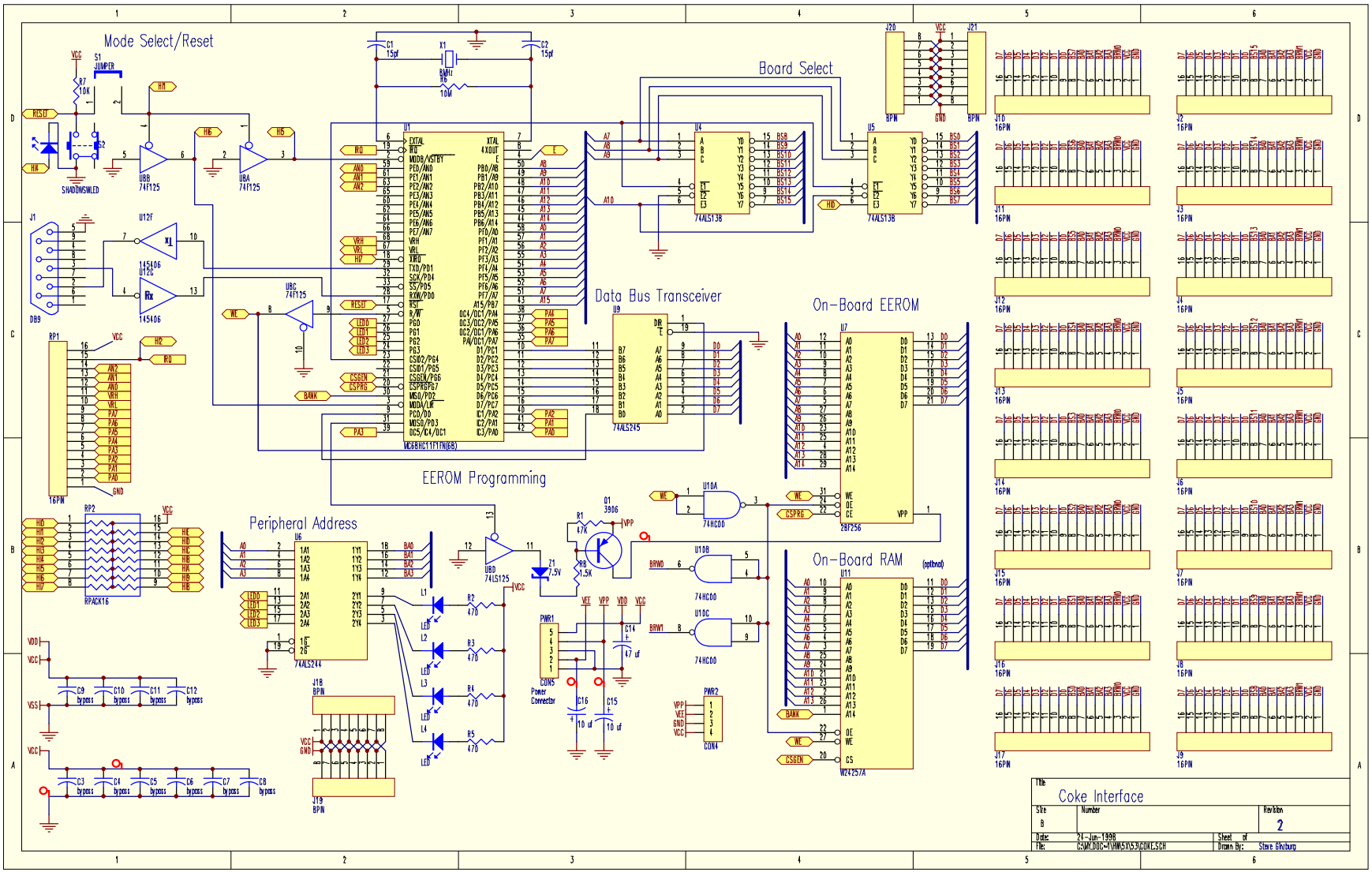
Mode Select/Reset
Board Select
Data Bus Transceiver
On-Board EEROM
EEROM Programming
Peripheral Address
On-Board RAM
Coke Interface
Site
B
Number
Revision
2
Date: 24-Jun-1996
Sheet of
File: C:\MYDOC~1\WMSYS3\COKE.SCH
Drawn By: Steve Ginzburg